Digital SQUID Magnetometers for Read-out of Detectors and Magnetic Particles

Department of Energy - Office of Nuclear Physics

Contract # DE-SC0007659

Dr. Masoud Radparvar HYPRES, Inc. 175 Clearbrook Rd. Elmsford, NY 10523

August 7, 2014

Outline

Superconducting Technology Overview

Company Overview

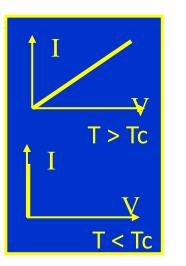
DOE Program Goals, Approach, and Accomplishments



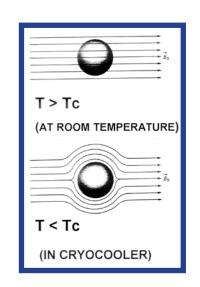
Superconductivity, Josephson Junctions, and SQUIDs

Superconductivity

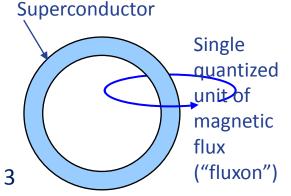
Zero Resistance



Expulsion of Magnetic Flux



Magnetic Flux Quantization

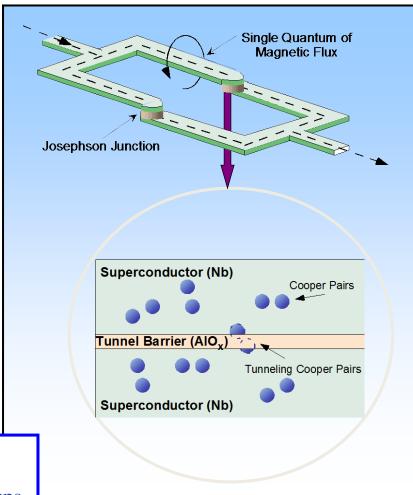


$$\Phi = \int B_n dA = n \Phi_0$$

$$\Phi_0 = h/2e = 2.07 \text{ mV} \cdot \text{ps}$$

$$= 2.07 \times 10^{-15} \text{ Wb}$$
Single Flux Quantum (SFQ)

Josephson Junctions, and SQUIDs





Superconductor Electronics Benefits

- 100+ GHz speed
- Low-power Dissipation
- Digital and mixed-signal
- Ideal transmission lines
- Low-noise (Quantum limit)
- Quantum accuracy (voltage standard and ADC)
- Hybrid super/semi capability
- Simple fabrication



HYPRES, Inc. - Elmsford, NY

- Founded in 1983 as spin-off from IBM; 19,000 sq. ft. 30 miles north of New York City
- US Privately held 33 employees, primarily advanced degree engineers and scientists
- World leader in Superconductor Microelectronics technology producing high-end instrumentation equipment
- Pursuing applications and working on existing projects in DOD, DOE, NASA, and NIH
- The only commercial foundry service for superconducting electronics





Mission and Strategic Focus

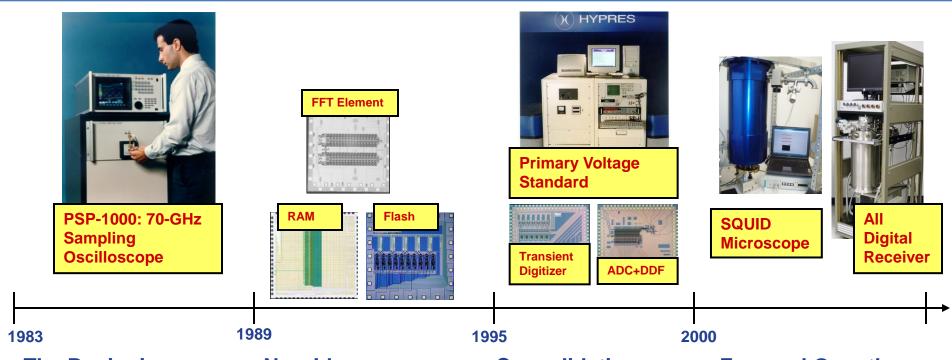
Mission

Develop and deploy innovative receivers, sensors, and high performance computing solutions based on superconducting circuits and cryoelectronics

Strategic Focus

- Wideband digital RF receivers based on analog to digital converters (ADCs)
- Superconducting QUantum Interference Device (SQUID)-based magnetic sensors for detectors and biomedical applications
- Custom chip and system design





The Beginning

- Developed technology for instrumentation markets
- ➤ 1st product commercialized
- ➤ Worlds fastest Scope

New Ideas

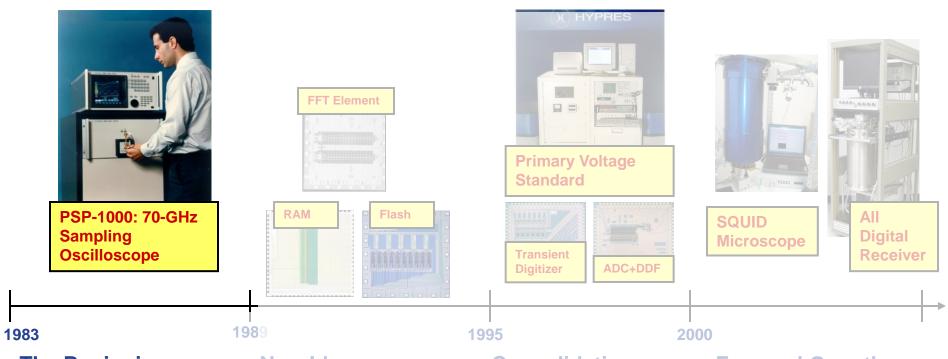
- Family of Superconductor Chips
- Analog to digital conversion
- > Commercial foundry

Consolidation

- World leader in Superconducting Technology
- > Voltage Standard

- Dual Use Military and Commercial Technology
- > SQUID Microscope





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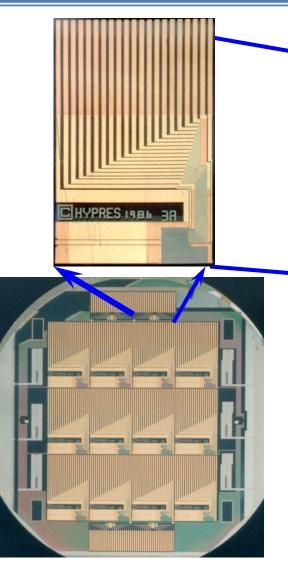
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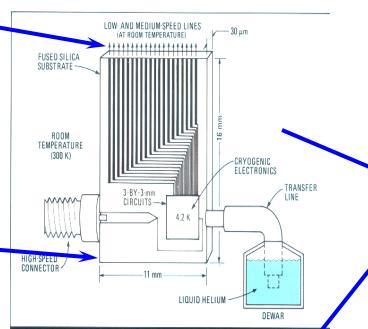
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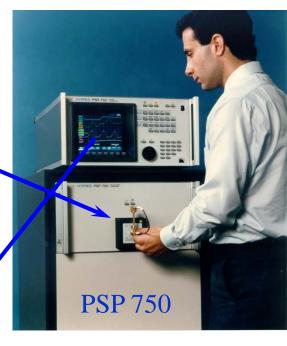
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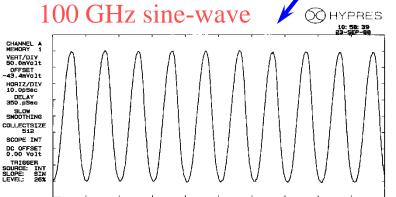


Sampling Oscilloscope



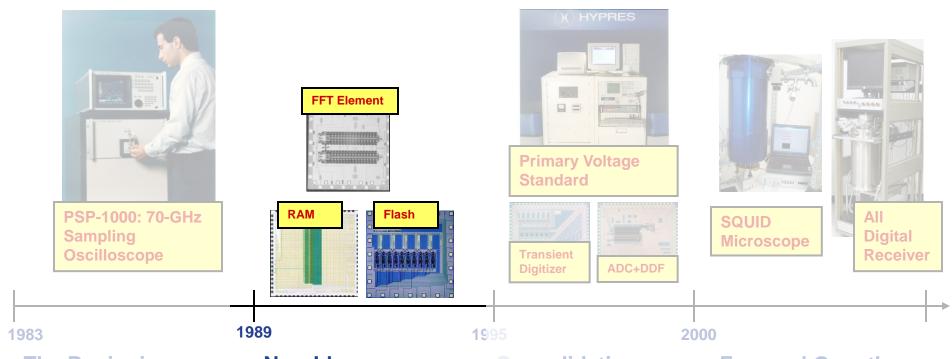






70 GHz bandwidth50 μV sensitivity





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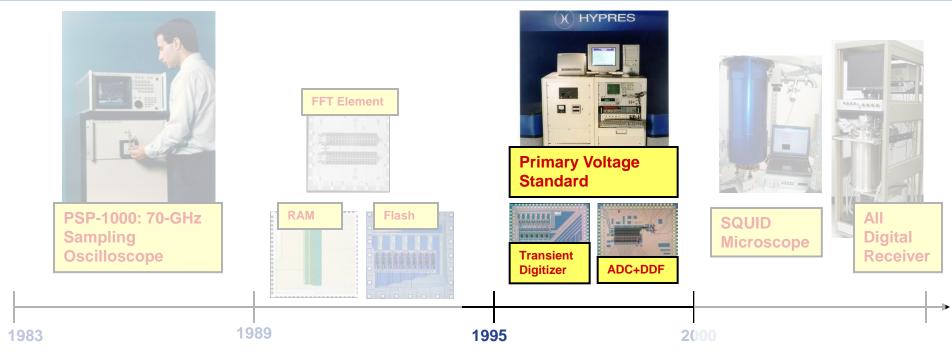
Customers, Collaborators, and Supporters

More than 75% of superconducting IC chips worldwide are produced by Hypres.

More than 80% of metrology labs worldwide utilize Hypres Voltage Standard Chips.

IVIC	ore than 60 % or metrology	iau	s worldwide utilize riypres	VOII	lage Standard Chips.
	BAE		Netherlands Institute for Standards		Colgate Univ.
	Raytheon		Bureau International Des Poids Mesures		SIMIT
	Northrop/Grumman		Centro Espanol de Metrologia, Spain		Saitama Univ.
	Argon ST		NIMTT, PR China		Columbia Univ.
	Lockheed/Martin		Danish Institute of Fundamental Metrology		Moscow State Univ.
	Cubic		Dura, Inc.		Univ. of Waterloo
	Titan		CSIRO Div. of Applied Physics, Australia		Univ. of Maryland
	ViaSat		Norfolk Naval Base		Univ. of Southampton
	Boeing		Fluke Corporation		Chalmers Univ.
	EADS Astrium		Research & Manufacturing, Inc.(JEMIC)		Dynetics
	Rockwell/Collins		G&G, Japan (JEMIC)		Tubigen Univ.
	Harris		Research & Manufacturing, Inc.(Malaysia)		Raytheon – BBN
	SwRI		Hong Kong		Univ. of North Carolina
	STI		Research & Manufacturing, Inc.(Mexico)		karlsruhe institute of technology
	L-3		IEN, Italy		MIT
	Thales		Research and Manufacturing, (Thailand)		chalmers university of technology
	Sarnoff Labs		Industrial Research Limited – New Zealand		Paul Scherrer Institute
	Ball Aerospace		Sandia Labs		Univ. of Wisconsin
	General Dynamics		Industrial Technology Research Institute		Max Planck Institute
	FMC/Selex		Taiwan		Univ. of Oxford
	DOC/NIST		Singapore Prod. & Stds. Bd.		Tech. Univ. Munchen
	NIH		INMETRO (Brazil)		Teratec Corp.
	NSF		Sirim Malaysia		Center for Astro Berkeley
	DOE		Inst. Nat. Meas., Canada		Niki Glass Co.
	NASA		Swiss Office of Federal Metrology		European Space Agency
	NSA		INTA Spain		Univ. of Heidelberg
	Las Alamos		Laz Representaciones, Spain (CEM)		Univ. of Colorado
	Advantest		Metrology SA – Greece		Lawrence Livermore
	National Measurement Service, Norway		Stanford Univ.		San Francisco State Univ.
	BIP – Russia		Univ. of California		SRON
	National Physical Laboratory, UK		Stoney Brook Univ.		Yale Univ.
	BNM/LCIE France		Univ. of Rochester		LETI OO LIVEDI

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New Ideas

- Family of Superconductor Chips
- Analog to digital conversion
- > Commercial foundry

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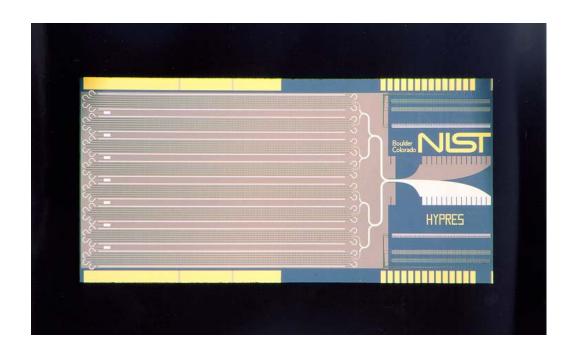
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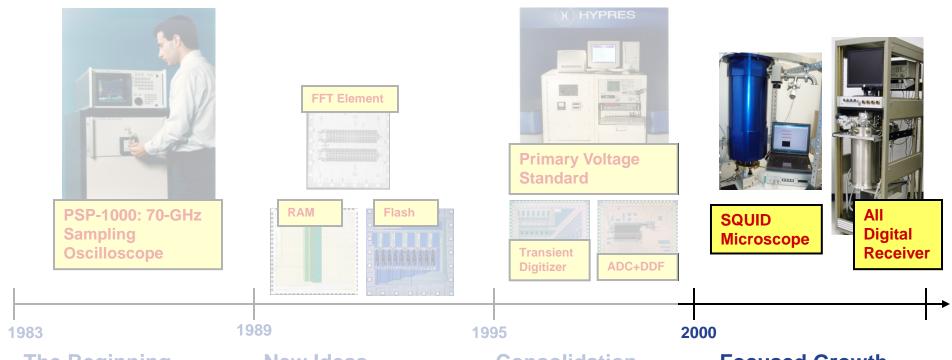


Primary Voltage Standard System

- Commercial Primary Voltage Standard for Metrology Markets Developed with DoD Dual use Resources
- This application cannot be done using any other technology







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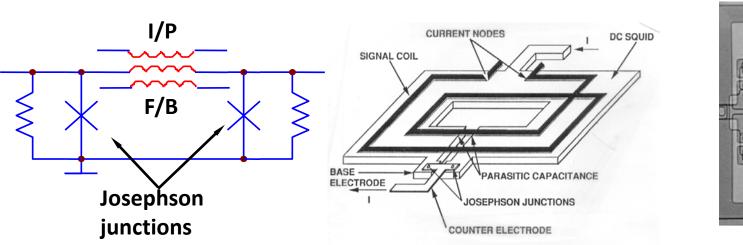
Consolidation

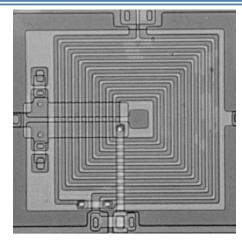
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SQUID (Superconducting QUantum Interference Device)

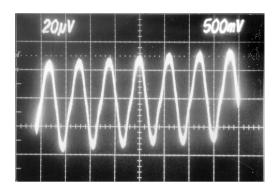




Current (10 µA/div.)

Voltage (10 µV/div.)

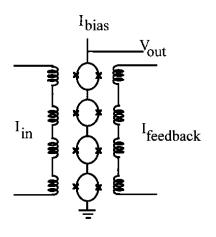
Voltage (20 μ V/div.)



I/P Current (1 µA/div.)



SQUID Magnetometer/Amplifier



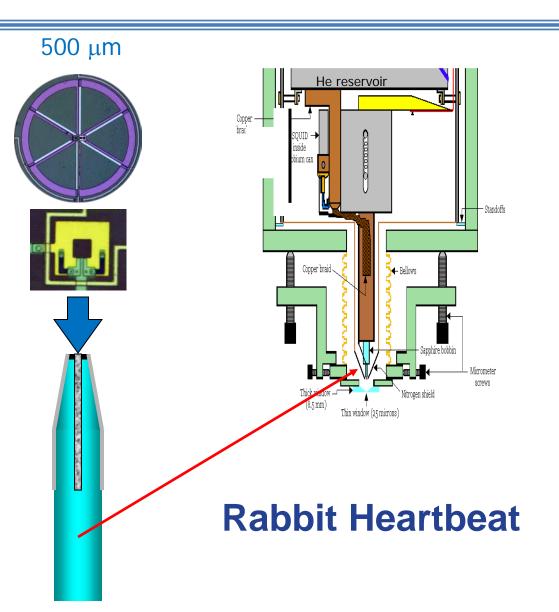
- Simple peripheral electronics
- Wide bandwidth amplifier
- Extremely low power dissipation
- Multi-channel

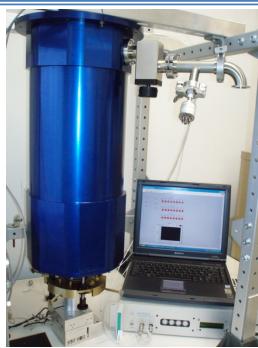


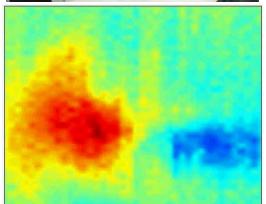




SQUID Microscope









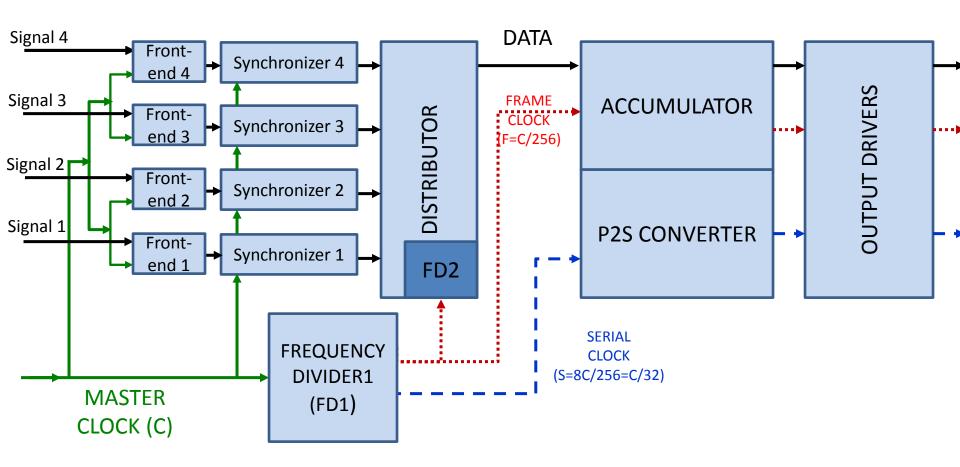
Objective

Develop a 4-channel digital SQUID (Superconducting QUantum Interference Device)-based amplifier system for read-out of detectors.

- Front-end is an analog SQUID with magnetic field sensitivity of ~6 x 10⁻²¹ Wb/√Hz
- Analog SQUID is followed by ADCs (Analog to Digital Converters) and multiplexers for on-chip data streaming and coupling to slower data acquisition electronics
- On-chip processing of the 4-channel data at ~20 GHz allows multiplexing of 100s of channels

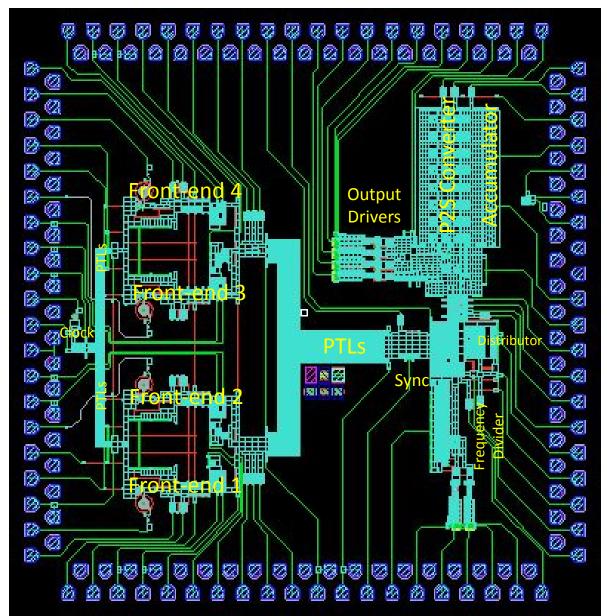


Schematic of 4-Channel Read-out Circuit



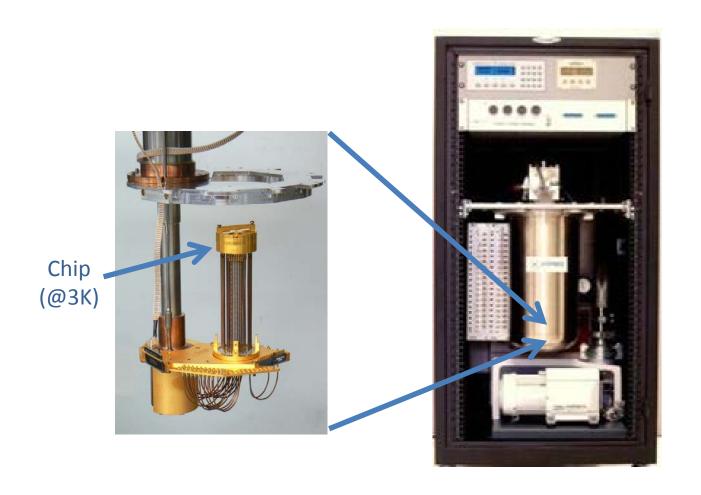


Layout of 4-Channel Read-out Circuit





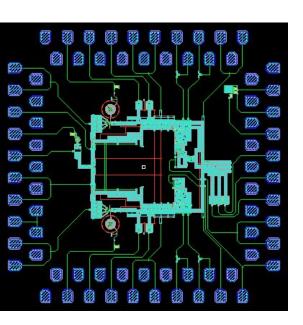
Cryogenic Package / Peripheral Electronics

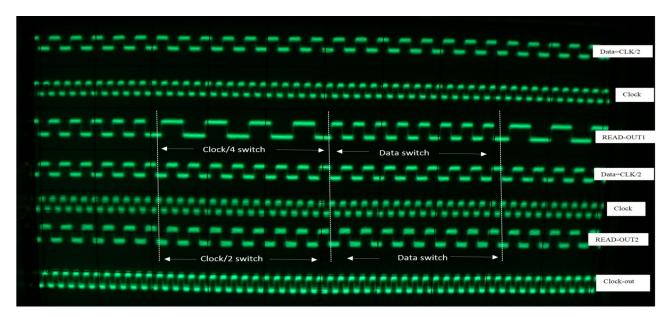




Diagnostic Circuits: Front-ends

Experimental Results

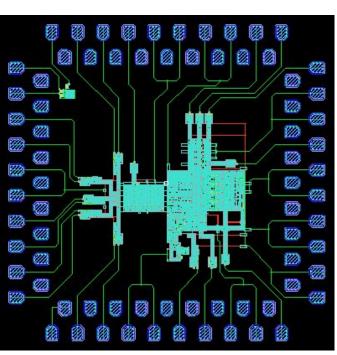


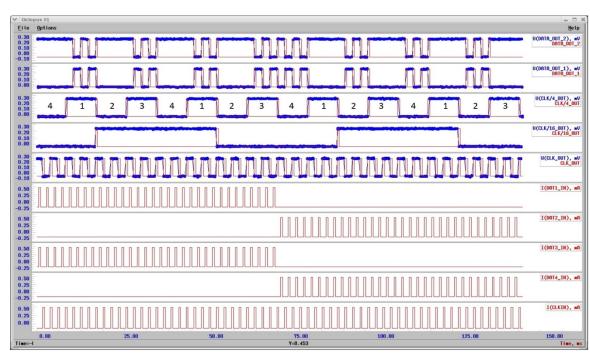


Test result of two-input front-end. The first three waveforms correspond to signal at Front-end 1 and next three waveforms correspond to signal at Front-end 2. The last waveform is clock-out propagation. Readout is generated by applying two square wave at 5 KHz and 180 degree phase difference to two switches of the each circuit. Switch 1 of Circuit 1 will allow propagation of Clock divided by 4 pulses, while Switch 1 of Circuit 2 allows propagation of Clock divided by 2 pulses. Switch 2 in both circuits allow Data propagation.

Diagnostic Circuit: Multiplexer

Experimental Results



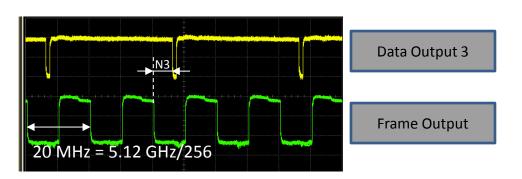


Low frequency functional test of the 4-to-1 multiplexer. The inputs are applied through channel Dat1_in, Dat2_in, Dat3_in and Dat4_in while CLK/4_OUT represents the selected channel as shown in the figure. The output replicates as Data_out_1 and Data_out_2.



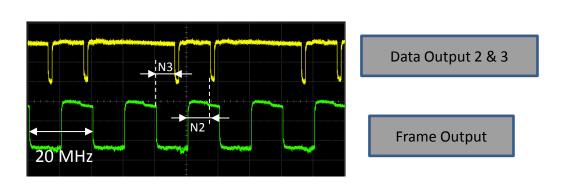
4-Channel Read-out Circuit

Experimental Results: Digital bits



Digital data output for input line 3.





Digital data outputs for input lines 2 & 3. The timing of the signals after transition in frame output identifies the input.



Accomplishments

- Completed the design, simulation, fabrication of the first iteration of the 4-channel digital SQUID amplifier chip as well as two of its diagnostic chips.
- Diagnostic chips were fully characterized. All components of the amplifier chip (pickup coil, front-end SQUID, analog to digital converter, multiplexer, etc.) successfully passed all tests.
- First iteration of the 4-channel digital SQUID amplifier was evaluated. The optimized 4-channel amplifier is currently being fabricated and is expected to become available by October 2014.

