Department of Energy Announces \$179 Million for for Microelectronics Science Research Centers

Announcement Number: LAB 24-3320 List Posted: 12/23/2024

Announcement Number.	DAD 24-3320	election for award negotiations is not a commitment by DOE to issue a	nn award or provide funding.		List Posteu.	12/23/2024
Center	Principal Investigator	Title	Institution	City	State	ZIP Code
Co-design and Heterogeneous Integration in Microelectronics for Extreme Environments (CHIME)	Braga, Davide	Single Photon Detectors Integrated with Cryogenic Electronics (SPICE)	Fermi National Accelerator Laboratory (FNAL)	Batavia	IL	60510-5011
Co-design and Heterogeneous Integration in Microelectronics for Extreme Environments (CHIME)	Fahim, Farah	VIAS: Vertically Integrated Artificial Intelligence for Sensing and High Performance Computing	Fermi National Accelerator Laboratory (FNAL)	Batavia	IL	60510-5011
Co-design and Heterogeneous Integration in Microelectronics for Extreme Environments (CHIME)	Hollingsworth, Jennifer	Nano Solutions On-Chip (NSOC)	Los Alamos National Laboratory (LANL)	Los Alamos	NM	87544-0600
Co-design and Heterogeneous Integration in Microelectronics for Extreme Environments (CHIME)	Stacey, Alastair	Exploring the limits of electron and phonon transport in diamond: nanoscale devices and extreme environments	Princeton Plasma Physics Laboratory (PPPL)	Princeton	NJ	08542-0451
Extreme Lithography & Materials Innovation Center (ELMIC)	Guha, Supratik	Ultra-dense Memory: Atom Scale Material Dynamics and System Consequences	Argonne National Laboratory (ANL) Pacific Northwest	Lemont	IL	60439-4803
Extreme Lithography & Materials Innovation Center (ELMIC)	Harilal, Sivanandan	Accelerating Next-Generation EUV Lithography (ANGEL)	National Laboratory (PNNL) Princeton Plasma	Richland	WA	99352-1793
Extreme Lithography & Materials Innovation Center (ELMIC)	Raitses, Yevgeny	Plasma-enabled 2D materials for energy-efficient microelectronics	Physics Laboratory (PPPL) Lawrence Livermore	Princeton	NJ	08542-0451
Extreme Lithography & Materials Innovation Center (ELMIC)	Reagan, Brendan	High Conversion Efficiency 2um Laser-Driven Sources for EUV Lithography and Plasma Science	National Laboratory (LLNL)	Livermore	CA	94551-0808
Microelectronics Energy Efficiency Research Center for Advanced Technologies (MEERCAT)	Ang, James	Democratization of Co-design for Energy-Efficient Heterogeneous Computing: DeCoDe	Pacific Northwest National Laboratory (PNNL)	Richland	WA	99352-1793
Microelectronics Energy Efficiency Research Center for Advanced Technologies (MEERCAT)	Deptuch, Grzegorz	El-Pho: Electro-Photonic Integrated Platform for Near-Sensor Processing in Extreme Environments	Brookhaven National Laboratory (BNL)	Upton	NY	11973-5000
Microelectronics Energy Efficiency Research Center for Advanced Technologies (MEERCAT)	Dragone, Angelo	Adaptive UltRa-fast Energy-efficient Intelligent Sensing technologies (AUREIS)	SLAC National Accelerator Laboratory	Menlo Park	CA	94025-7015
Microelectronics Energy Efficiency Research Center for Advanced Technologies (MEERCAT)	Garcia-Sciveres, Maurice	Nanoscale hybrids: a new paradigm for energy-efficient optoelectronics	Lawrence Berkeley National Laboratory (LBNL)	Berkeley	CA	94720-8099
Microelectronics Energy Efficiency Research Center for Advanced Technologies (MEERCAT)	Johnson, Grant	Self-Assembly of Tunable Molecular Memristors with Long- Range Order for Resilient and Energy-Efficient Neuromorphic Computing	Pacific Northwest National Laboratory (PNNL)	Richland	WA	99352-1793
Microelectronics Energy Efficiency Research Center for Advanced Technologies (MEERCAT)	McIntyre, Paul	Enabling Science for Transformative Energy-Efficient Microelectronics (ESTEEM)	SLAC National Accelerator Laboratory	Menlo Park	CA	94025-7015
Microelectronics Energy Efficiency Research Center for Advanced Technologies (MEERCAT)	Nelson, Jeffrey	Nano-Scale Research Center for Heterogeneous Integration Platforms (NSR-CHIP)	Sandia National Laboratories, New Mexico (SNL-NM)	Albuquerque	NM	87185-0100
Microelectronics Energy Efficiency Research Center for Advanced Technologies (MEERCAT)	Taylor, Valerie	BIA: A Co-Design Methodology to Transform Materials and Computer Architecture Research for Energy Efficiency	Argonne National Laboratory (ANL)	Lemont	IL	60439-4803